

Appl. N . 09/802,020  
Amdt. dated February 4, 2004  
Reply to Office Action of November 4, 2003

PATENT

**Amendments to the Specification:**

*Please replace paragraph on page 1, line 5, with the following amended paragraph:*

**CROSS-REFERENCES TO RELATED APPLICATIONS**

This application is being filed concurrently with related U.S. patent applications: ~~Attorney Docket Number 016747-00991 U.S. Patent Application No. 09/802,017~~, entitled "VLIW Computer Processing Architecture with On-chip DRAM Usable as Physical Memory or Cache Memory"; ~~Attorney Docket Number 016747-01001 U.S. Patent Application No. 09/802,289~~, entitled "VLIW Computer Processing Architecture Having a Scalable Number of Register Files"; ~~Attorney Docket Number 016747-01780 U.S. Patent Application No. 09/802,108~~, entitled "Computer Processing Architecture Having a Scalable Number of Processing Paths and Pipelines"; ~~Attorney Docket Number 016747-01051 U.S. Patent Application No. 09/802,324 now Issued Patent No. 6,631,439~~, entitled "VLIW Computer Processing Architecture with On-chip Dynamic RAM"; ~~Attorney Docket Number 016747-01211 U.S. Patent Application No. 09/802,120~~, entitled "VLIW Computer Processing Architecture Having the Program Counter Stored in a Register File Register"; ~~Attorney Docket Number 016747-01461 U.S. Patent Application No. 09/801,564~~, entitled "Processing Architecture Having Parallel Arithmetic Capability"; ~~Attorney Docket Number 016747-01471 U.S. Patent Application No. 09/802,196~~, entitled "Processing Architecture Having an Array Bounds Check Capability"; ~~Attorney Docket Number 016747-01481 U.S. Patent Application No. 09/802,121~~, entitled "Processing Architecture Having an Array Bounds Check Field Swapping Capability"; and, ~~Attorney Docket Number 016747-01531 U.S. Patent Application No. 09/802,291~~, entitled "Processing Architecture Having a Compare Capability"; all of which are incorporated herein by reference.

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